Taiwan Semiconductor

3A, 50V - 1000V Standard Surface Mount Rectifier

FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Low forward voltage drop
- High current capability
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- General purpose

MECHANICAL DATA

- Case: DO-214AB (SMC)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Polarity: Indicated by cathode band
- Weight: 0.250g (approximately)

KEY PARAMETERS			
PARAMETER VALUE UN			
١ _F	3	А	
V _{RRM}	50 - 1000	V	
I _{FSM}	100	А	
T _{J MAX}	150	°C	
Package	DO-214AB (SMC)		
Configuration	Single die		



Cathode -	– Anode
Cathode -	- Anoue

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)									
PARAMETER	SYMBOL	S3A-T	S3B-T	S3D-T	S3G-T	S3J-T	S3K-T	S3M-T	UNIT
Marking code on the device		S3A	S3B	S3D	S3G	S3J	S3K	S3M	
Repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Reverse voltage, total rms value	V _{R(RMS)}	35	70	140	280	420	560	700	V
Forward current	I _F				3				А
Surge peak forward current, 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	-ѕм 100			А				
Junction temperature	TJ	- 55 to +150			°C				
Storage temperature	T _{STG}	- 55 to +150			°C				



THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	R _{ƏJL}	8	°C/W	
Junction-to-ambient thermal resistance	R _{θJA}	56	°C/W	
Junction-to-case thermal resistance	R _{eJC}	12	°C/W	

Thermal Performance Note: Units mounted on PCB (16mm x 16mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
	I _F = 1.5A, T _J = 25°C		0.88	-	V
Forward voltage ⁽¹⁾	$I_F = 3.0A, T_J = 25^{\circ}C$	V _F	0.93	1.15	V
	I _F = 1.5A, T _J = 125°C		0.75	-	V
	$I_F = 3.0A, T_J = 125^{\circ}C$		0.81	0.92	V
Poweree europt @ reted \/ ⁽²⁾	T _J = 25°C		-	10	μA
Reverse current @ rated $V_R^{(2)}$	T _J = 125°C	– I _R	-	250	μA
Junction capacitance	1MHz, V _R = 4.0V	CJ	27	-	pF

Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION

ORDERING CODE ⁽¹⁾	PACKAGE	PACKING	
S3x-T	DO-214AB (SMC)	3,000 / Tape & Reel	

Notes:

1. "x" defines voltage from 50V(S3A-T) to 1000V(S3M-T)



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.1 Forward Current Derating Curve

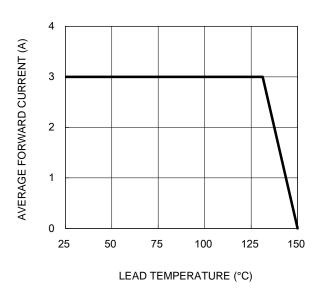


Fig.3 Typical Reverse Characteristics

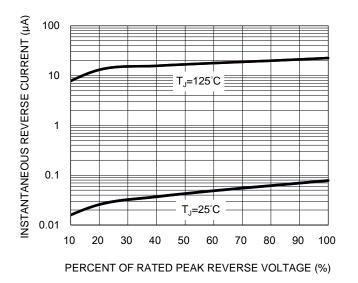


Fig.4 Typical Forward Characteristics

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REVERSE VOLTAGE (V)

100

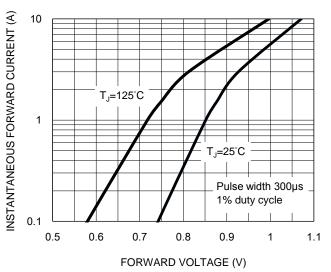


Fig.2 Typical Junction Capacitance

1000

100

10

1

1

f=1.0MHz

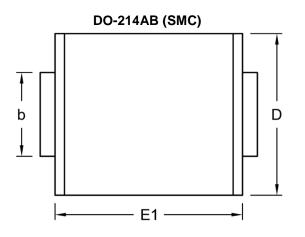
Vsig=50mVp-p

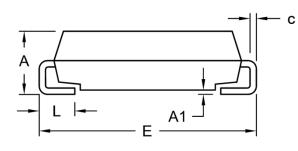
CAPACITANCE (pF)



TAIWAN SEMICONDUCTOR

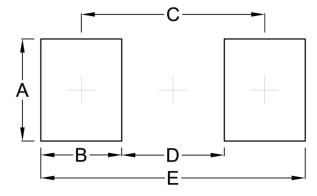
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DIM.	Unit (mm)		Unit	inch)	
	Min.	Max.	Min.	Max.	
A	1.99	2.61	0.078	0.103	
A1	0.10	0.20	0.004	0.008	
b	2.85	3.27	0.112	0.129	
с	0.15	0.31	0.006	0.012	
D	5.59	6.22	0.220	0.245	
E	7.75	8.13	0.305	0.320	
E1	6.60	7.11	0.260	0.280	
L	0.76	1.52	0.030	0.060	

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	3.82	0.150
В	3.03	0.119
С	6.87	0.270
D	3.84	0.151
E	9.90	0.390

MARKING DIAGRAM



- P/N = Marking Code
- G = Green Compound
- YW = Date Code
- F = Factory Code



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